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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/666,228	09/18/2003	Franz Hirler	MUH-12747 C	8480	
24131 7.	590 09/27/2004		EXAMINER		
	D GREENBERG, PA	PERALTA, GINETTE			
P O BOX 2480 HOLLYWOOI	0 D, FL 33022-2480		ART UNIT	PAPER NUMBER	
110221 001	s, 12 55022 2 100		2814		

DATE MAILED: 09/27/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
Office Action Summary		10/666,228	HIRLER ET AL.				
		Examiner	Art Unit	1			
		Ginette Peralta	2814	And			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1)	1) Responsive to communication(s) filed on						
2a) <u></u> ☐	☐ This action is FINAL . 2b) ☐ This action is non-final.						
3)□	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims							
4) Claim(s) 1-8 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-8 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.							
Applicati	on Papers						
9) The specification is objected to by the Examiner.							
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority u	ınder 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 10/392,024. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
Attachmen	t(s)						
	e of References Cited (PTO-892)	4) Interview Summary					
3) 🛛 Inform	e of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date 7/04,7/04,9/03.	Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:		O-152)			

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 2. Claims 1-8 are rejected under 35 U.S.C. 102(e) as being anticipated by Lanzerstorfer et al. (U. S. Pat. 6,605,841 B2).

The applied reference has a common assignee with the instant application.

Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention "by another," or by an appropriate showing under 37 CFR 1.131.

Lanzerstorfer et al. discloses in figs. 2H a power transistor having at least one trench transistor cell in a semiconductor body comprising a drain zone, a drift zone 122, a channel zone 14, and a source zone formed in each case successively and substantially horizontally in the semiconductor body; the semiconductor body having a trench formed therein with a base and a defined body height opposite a pn junction

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between the drift zone and the channel zone; a first dielectric layer 120 cladding the trench substantially to the body height, and a gate oxide 34A cladding the trench between the body zone and a semiconductor body surface; and a field electrode 40 extending in the trench substantially from the trench base to an upper edge of the first dielectric layer; a gate electrode 60 disposed substantially between the body height and the semiconductor body surface, the gate electrode 60 having a lower edge with a profile at least partly different from horizontal; and a second dielectric layer 50A formed between the gate electrode 60A and the field electrode 40A; wherein the profile of the lower edge of the gate electrode is at least partly angled relative to the semiconductor body surface, wherein the profile has a falling angle between two trenches, wherein the profile of the lower edge of the gate electrode is formed with at least one outward bulge, wherein the field electrode overlaps the gate electrode, wherein at least one of the field electrode and the gate electrode intersects and/or passes through a plane defined by the pn junction between the drift zone and the channel zone, wherein the field electrode is connected to be at a fixed potential, wherein the field electrode is connected to be at the source potential.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ginette Peralta whose telephone number is (571)272-1713. The examiner can normally be reached on Monday to Friday 8:00 AM- 5:30 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571)272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GP

LONG PHAM RIMARY EXAMINER